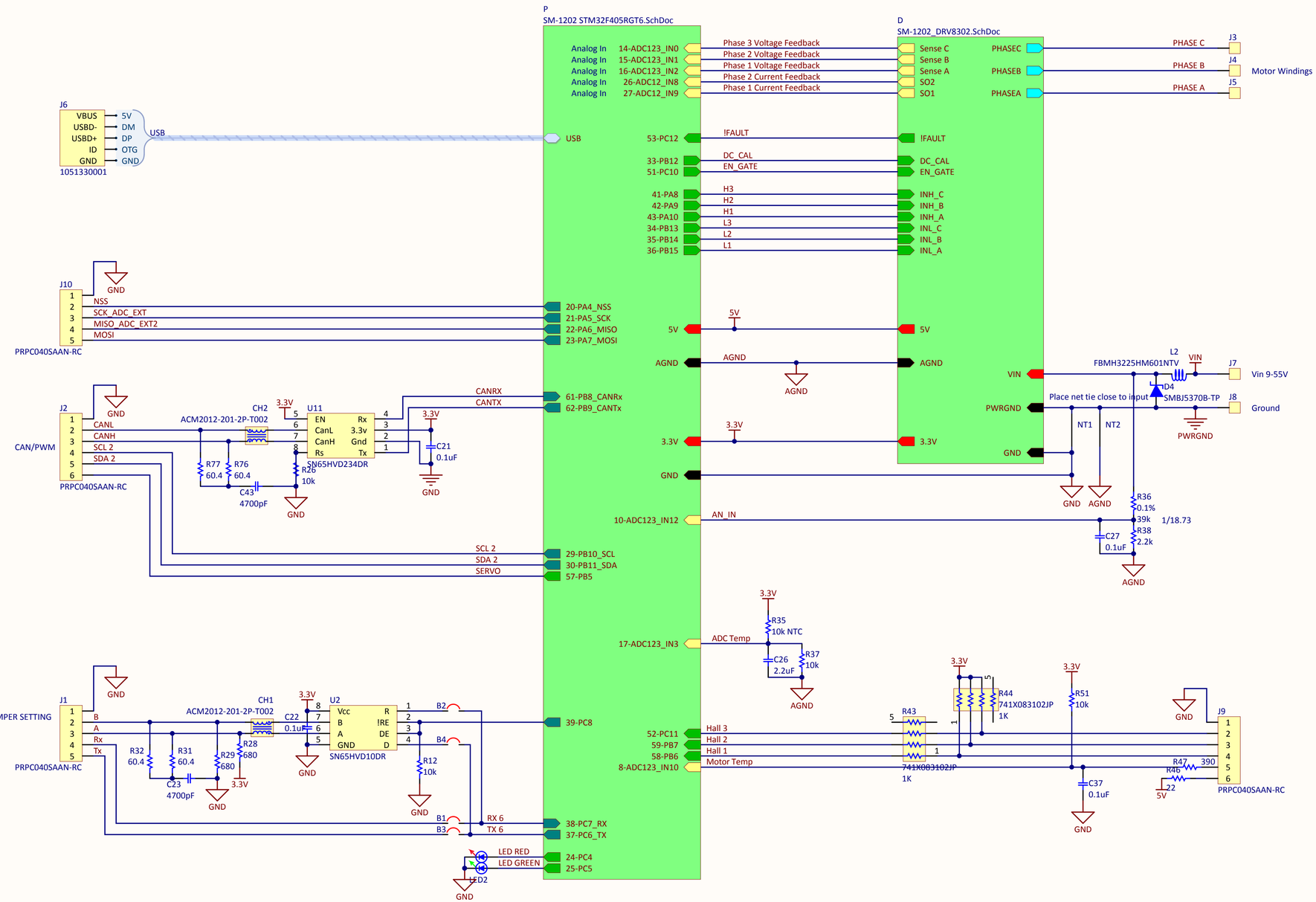
Change Log
Rev A 1/2018
Initial Release



Proprietary and Confidential	Approvals	Initials	Date	Dwg Title: THRUSTER ESC	
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	Checked	NJW		Project: ROV	Page: 1 of 3
	Mfg Appr				
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OC_ADJ - OC determined by comparing low side fet Vds and OC_ADJ input. OC_AJD is set to DVdd to disable overcurrent detection.

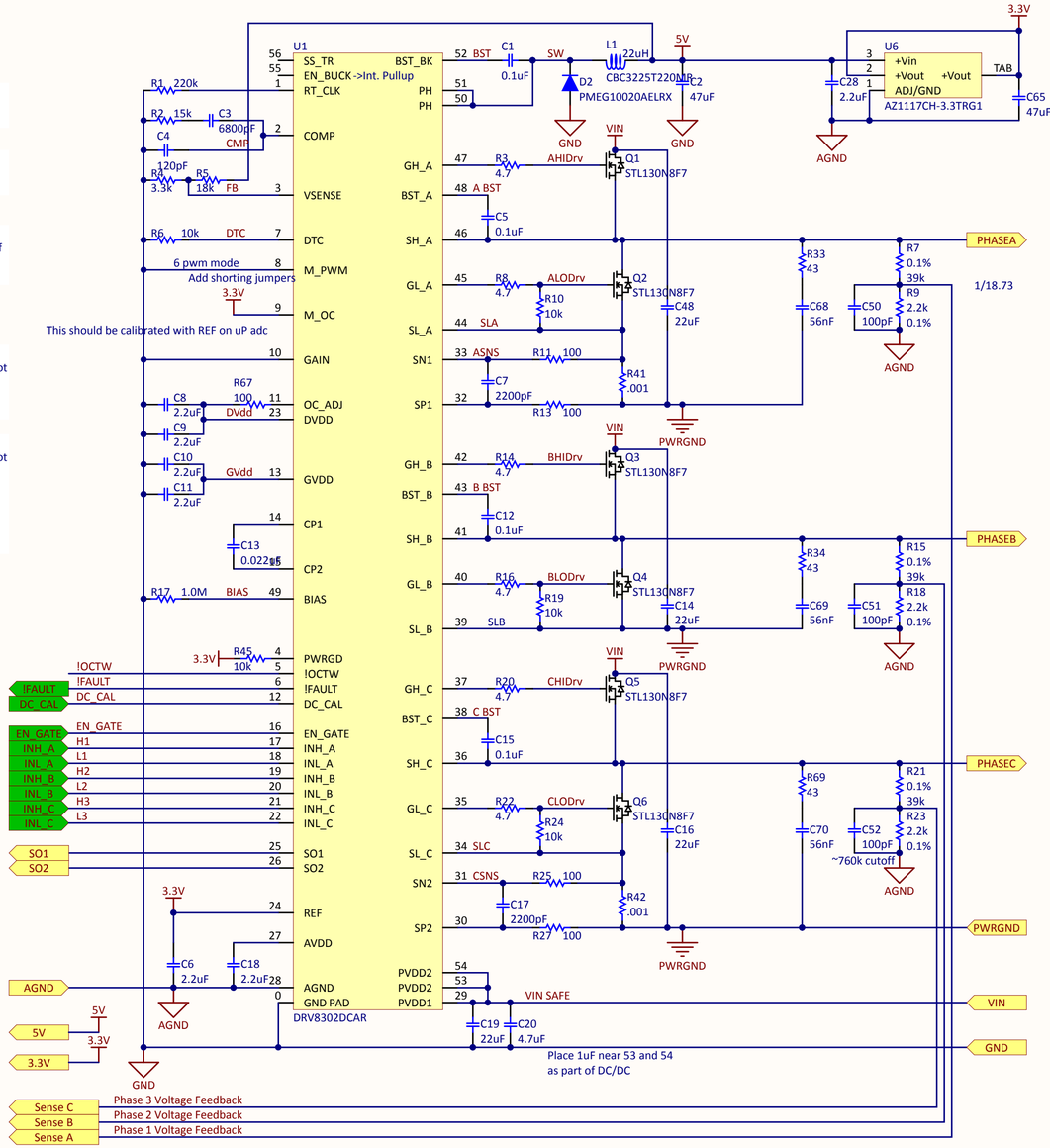
CP1 and CP2 - Gate drive charge pump. Internal TPS54160 is set to output 5V, charge pump doubles that to 10V@30mA for gate drive.

REF - Reference voltage to set output of shunt amplfiiers with a bias voltage which equals to half of the voltage set on this pin. Connect to ADC reference in microcontroller.

DVDD - Internal 3.3-V supply voltage. DVDD cap should connect to AGND. This is an output, but not specified to drive external circuitry.

AVDD - Internal 6-V supply voltage, AVDD cap should connect to AGND. This is an output, but not specified to drive external circuitry.

GAIN - LOW: 10V/V, HIGH: 40V/V
Total gain is .001 x 10 = .01 x Ip
Vo = Ip x .01 + Vref/2



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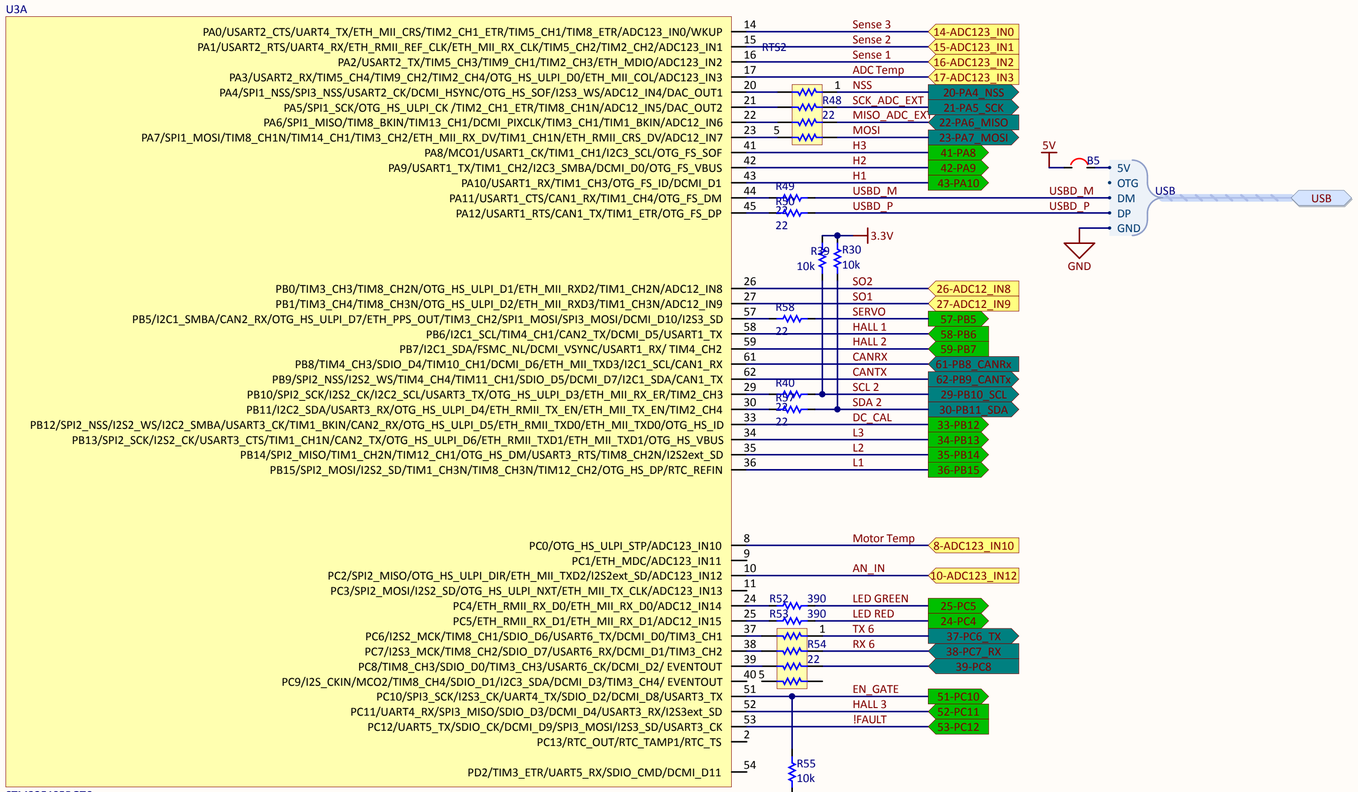

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